



ocket No.: 061352-0119

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Tohru SAITO, et al.

Application No.: 10/564,085

Filed: January 11, 2006

: Customer Number: 20277

: Confirmation Number: Not Yet Assigned

: Group Art Unit: Not Yet Assigned

: Examiner: Not yet assigned

For: HETEROJUNCTION BIPOLAR TRANSISTOR AND METHOD FOR
MANUFACTURING SAME

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the document listed on the attached form PTO-1449. It is respectfully requested that the document be expressly considered during the prosecution of this application, and that the document be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

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Respectfully submitted,

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INFORMATION DISCLOSURE CITATION IN AN APPLICATION  (PTO-1449)				ATTY. DOCKET NO. 061352-0119	SERIAL NO. 10/564,085	
				APPLICANT Tohru SAITO, et al.		
				FILING DATE January 11, 2006	GROUP 2811 Not Yet Assigned	
U.S. PATENT DOCUMENTS						
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code(s) (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		US				
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FOREIGN PATENT DOCUMENTS						
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Code-Number + Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes No
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				
/M.L./		Jagannathan, B., et al. "Self-Aligned SiGe NPN Transistors With 285 GHz f _{max} and 207 GHz f _T in a Manufacturable Technology" IEEE Electron Device Letters, Vol. 23, No. 5, May 2002, pp. 258-260.				
EXAMINER <i>Meiya Li</i>			DATE CONSIDERED 07/10/2009			

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered.

Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.